

**Notice of References Cited**Application/Control  
09/809,034Applicant(s)/Patent Under  
Reexamination  
KONDA ET AL.Examiner  
Clifford H KnollArt Unit  
2112

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2001/0051930	12-2001	Nakamura, Ikuo	705/56
	B	US-6,694,349 B1	02-2004	Zou, Feng	709/202
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Fuller, "Architecture for power management on high speed serial bus", P1394b working group document pmgtv72, 9/23/1998, 40 pp. ( <a href="http://grouper.ieee.org/groups/1394/b/docs1998.shtml">http://grouper.ieee.org/groups/1394/b/docs1998.shtml</a> )
	V	
	W	
	X	

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